

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1		Fundamental concepts of digital methodologies	1	CR		
2		Computer types & functional units	1	CR		
3		Basic Operational Concept.	1	CR		
4		Bus structure	1	CR		
5		Performance, Multiprocessor & Multicomputer.	1	CR		
6		data representation	1	CR		
7		Fixed point				
8		Floating point				
9		representation.				
10		Problems related to data representation.	1	CR		
11		Addition & Subtraction	11	CR		
12						
13		Multiplication Alg.	11	CR		
14		Signed & unsigned				
15						
16		Division Algorithm	11	CR		
17		Restoring Non-restoring				
18		Decimal arithmetic	11	CR		
19		operation				
20						
21		Fixed & Floating point operation	11	CR		
22		Register transfer Language	111	CR		
23		Register transfer bus & memory transfer	111	CR		
24						
25		microoperation arithmetic	111	CR		
26		ALU microoperation				
27		Shift microoperation	111	CR		
28		ALU Unit				
29		Computer register & Instruction	111	CR		

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30 31		Addressing mode	III	CR		
32		Instruction format	III	CR		
33		Processor organization	III	CR		
34		Memory hierarchy	IV	CR		
35		main memory	IV	CR		
36		Auxiliary memory	IV	CR		
37 38		Virtual memory MM hardware	IV	CR		
39 40		Associative memory	IV	CR		
41 42		Cache memory	IV	CR		
43		Input output interface	IV	CR		
44 45		Data transfer modes	IV	CR		
46		Direct memory access	IV	CR		
47		Input output processor	IV	CR		
48		Parallel processing	V	CR		
49 50		Pipelining Arithmetic	V	CR		
51		Instruction pipeline	V	CR		
52		RISC & CISC processor	V	CR		
53		multiprocessor	V	CR		

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