Aditya Institute of Technology and Management (Autonomous), Tekkali
III Year B.Tech (Electronics and Communication Engineering) – I[st] Sem.

LINEAR IC APPLICATIONS
LESSON PLAN

Periods	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
1	29.06.16	**Introduction:** Introduction to integrated circuits, Differential Amplifier.	Unit I	PPT		
2	30.06.16 01.07.16	DC and AC analysis of dual input and balanced output configuration		Chalk & Board		
4	07.07.16	Properties of other differential amplifier configuration (dual Input unbalanced output)		Chalk & Board		
5	13.07.16	Single ended input – balanced/unbalanced output		Chalk & Board		
7	14.07.16	DC coupling and cascade differential amplifier stages		Chalk & Board		
9	15.07.16	Level translator,		Chalk & Board		
10	03.08.16	Characteristics of OP–amps	Unit II	PPT		
11	04.08.16	Integrated circuits – types, Classification,		PPT		
12	05.08.16	Package types and temperature ranges, Op-amp block diagram		PPT		
13	10.08.16	ideal and practical Op-amp specifications		PPT		
14	10.08.16	DC characteristics,		Chalk & Board		
15	11.08.16	AC characteristics		Chalk & Board		
16	12.08.16	741 op-amp & its features		PPT		
17	17.08.16	FET input Op-amps		Chalk & Board		
18	17.08.16	Op-amp parameters and measurement		Chalk & Board		
19	18.08.16	Frequency compensation technique		Chalk & Board		
20	19.08.16	**Linear applications of Op-amps:** Inverting and non-inverting amplifier	Unit III	Chalk & Board		
21	24.08.16	Integrator		Chalk & Board		
22	24.08.16	differentiator,		Chalk & Board		
23	25.08.16	difference amplifier		Chalk & Board		
24	26.08.16	Instrumentation amplifier		Chalk & Board		
25	31.08.16	AC amplifier		Chalk & Board		
26	31.08.16	V to I, I to V converters, buffers		Chalk & Board		
27	01.09.16	**Non–linear applications of Op–amps:**		Chalk & Board		
28	01.09.16	Non-linear function generation		Chalk & Board		
29	02.09.16	Comparators		Chalk & Board		
30	07.09.16	Multivibrators		Chalk & Board		
31	07.09.16	log and anti log amplifiers		Chalk & Board		
32	08.09.16	Precision rectifiers.	Unit IV	Chalk & Board		
33	09.09.16	**Active Filters:** Introduction		Chalk & Board		
34	14.09.16	Butterworth filters – 1st order, 2nd order LPF, HPF filters		Chalk & Board		
35	14.09.16	Band pass, Band reject and all pass filters.		Chalk & Board		
36	15.09.16	**D to A and A to D converters**		Chalk & Board		
37	16.09.16	: Introduction basic DAC techniques		Chalk & Board		
38	21.09.16	Weighted resistor DAC.		Chalk & Board		
39	21.09.16	R-2R ladder DAC		PPT		
40	22.09.16	inverted R-2R DAC,		PPT		
41	23.09.16	Different types of ADCs: parallel comparator, counter type		Chalk & Board		
42	28.09.16	Successive approximation		PPT		
43	28.09.16	dual slope ADCs		Chalk & Board		
44	29.09.16	DAC and ADC Specifications,		Chalk & Board		
45	30.09.16	Specifications of ADC 574, DAC 1408		Chalk & Board		
46	05.10.16	**Timers and Phase Locked Loops**	Unit V	PPT		
47	05.10.16	Introduction to 555 timer, functional diagram		Chalk & Board		
48	06.10.16	Monostable and astable operations and applications		Chalk & Board		
49	07.10.16	Schmitt Trigger.		Chalk & Board		
50	13.10.16	PLL - introduction, block schematic		Chalk & Board		
51	14.10.16	Principles and description of individual blocks		Chalk & Board		
52	19.10.16	565 PLL		Chalk & Board		
53	19.10.16	Applications of PLL – frequency multiplication,		Chalk & Board		
54	20.10.16	Frequency translation		Chalk & Board		
55	21.10.16	AM, FM and FSK demodulators		Chalk & Board		
56	26.10.16	Applications of VCO (566).		Chalk & Board		

LESSON PLA

57	26.10.16	**Analog Multipliers and Modulators:** Four quadrant multiplier		Chalk & Board	
58	27.10.16	balanced modulator, IC1496		Chalk & Board	
59	28.10.16	applications of analog switches and multiplexers		Chalk & Board	
60	28.10.16	sample and hold amplifiers		Chalk & Board	
				Chalk & Board	

Signature of the faculty

Signature of HOD/ECE